Application No.: 09/922,484

2

Docket No.: 04303/100N160-US1

Group III - Claims 24-28 drawn to a configurable modulator comprising a memory having shift register taps and a selective interconnect coupled to a part of the shift register taps, and an input line coupled to a multiplexer.

Applicant hereby elects Group I (Claims 1-19 and 29-38).

An action on the merits of all the elected claims and a Notice of Allowance thereof are respectfully requested.

Dated: December 27, 2004

Respectfully submitted,

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